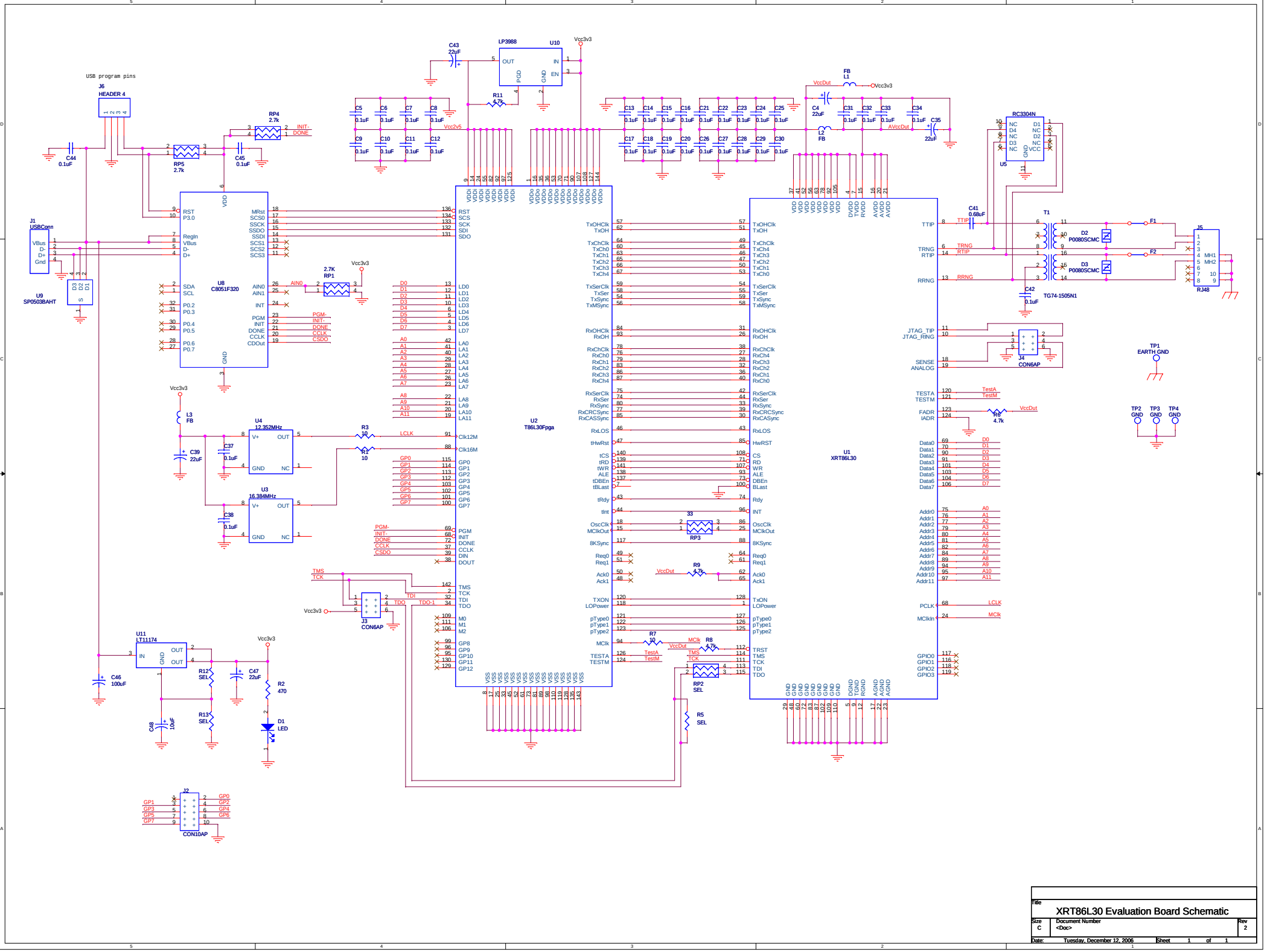
USB program pins
J6
HEADER 4
C44
0.1uF
RP5
2.7k
RP4
2.7k
C45
0.1uF
INIT-
DONE
J1
USBConn
U9
SP0503BAHT
U8
C8051F320
RST
P3.0
RegIn
VBus
D-
D+
SDA
SCL
P0.2
P0.3
P0.4
P0.5
P0.6
P0.7
MRst
SCS0
SSCK
SSDO
SSDI
SCS1
SCS2
SCS3
AIN0
AIN1
INT
PGM
INIT
DONE
CCLK
CDOut
VDD
GND
2.7K
RP1
Vcc3v3
L3
FB
C39
22uF
U4
12.352MHz
U3
16.384MHz
C37
0.1uF
C38
0.1uF
V+
OUT
GND
NC
R3
10
R1
10
LCLK
U11
LT11174
IN
OUT
C46
100uF
C48
10uF
R12
SEL
R13
SEL
C47
22uF
R2
470
D1
LED
J2
CON10AP
GP0
GP1
GP2
GP3
GP4
GP5
GP6
GP7
C43
22uF
LP3988
U10
PGD
GND
EN
R11
4.7k
Vcc2v5
C5
C6
C7
C8
C9
C10
C11
C12
0.1uF
C13
C14
C15
C16
C17
C18
C19
C20
C21
C22
C23
C24
C25
C26
C27
C28
C29
C30
C31
C32
C33
C34
C35
VccDut
FB
L1
C4
22uF
L2
AVccDut
RC3304N
U5
NC
D1
D2
D3
D4
VCC
C41
0.68uF
TTIP
TRNG
RTIP
RRNG
T1
TG74-1505N1
C42
0.1uF
D2
P0080SCMC
D3
P0080SCMC
F1
F2
J5
RJ48
MH1
MH2
JTAG_TIP
JTAG_RING
J4
CON6AP
SENSE
ANALOG
TESTA
TESTM
TestA
TestM
FADR
IADR
R6
4.7k
TP1
EARTH GND
TP2
GND
TP3
GND
TP4
GND
U2
T86L30Fpga
U1
XRT86L30
TxOHClk
TxOH
TxChClk
TxCh0
TxCh1
TxCh2
TxCh3
TxCh4
TxSerClk
TxSer
TxSync
TxMSync
RxOHClk
RxOH
RxChClk
RxCh0
RxCh1
RxCh2
RxCh3
RxCh4
RxSerClk
RxSer
RxSync
RxCRCSync
RxCASync
RxLOS
tHwRst
tCS
tRD
tWR
ALE
tDBEn
tBLast
tRdy
tInt
OscClk
MClkOut
8KSync
Req0
Req1
Ack0
Ack1
TXON
LOPower
pType0
pType1
pType2
MClk
TESTA
TESTM
Clk12M
Clk16M
GP0
GP1
GP2
GP3
GP4
GP5
GP6
GP7
PGM
INIT
DONE
CCLK
DIN
DOUT
TMS
TCK
TDI
TDO
M0
M1
M2
GP8
GP9
GP10
GP11
GP12
VSS
RP3
33
R9
4.7k
R7
10
R8
4.7k
RP2
SEL
R5
SEL
CS
RD
WR
ALE
DBEn
BLast
Rdy
INT
OscClk
MClkOut
8KSync
Req0
Req1
Ack0
Ack1
TXON
LOPower
pType0
pType1
pType2
TRST
TMS
TCK
TDI
TDO
TTIP
TRNG
RTIP
RRNG
SENSE
ANALOG
TESTA
TESTM
FADR
IADR
Data0
Data1
Data2
Data3
Data4
Data5
Data6
Data7
Addr0
Addr1
Addr2
Addr3
Addr4
Addr5
Addr6
Addr7
Addr8
Addr9
Addr10
Addr11
PCLK
MClkin
GPIO0
GPIO1
GPIO2
GPIO3
GND
DGND
TGND
RGND
AGND
AVDD
DVDD
TVDD
RVDD
Title
XRT86L30 Evaluation Board Schematic
Size
C
Document Number
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Rev
2
Date:
Tuesday, December 12, 2006
Sheet
1
of
1